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Urgent

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Date: 1-Nov-05

To:

Examiner: Syed J. Ali

Fax:

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Art Unit:

2127

USPTO

From:

Fax:

M/S:

Stuart A. Whittington

Intel Corporation

Subject: Application No.: 10/017,047

Docket #: 42390P12401 Inventor: Matthew A. Hayduk Filed: 12/13/2001

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1450 on November 1, 2005.

Lisa M. Hopkinson

Date: 11/1/05

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PTO/SB/21 (09-04)
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			Application Number	1	10/017,047 December 13, 2001			
TRANSMITTAL		Filing Date	Dece					
FORM		First Named Inventor	HAYE	HAYDUK, Malihew A.				
		Art Unit	2127	2127				
(to be used for all correspondence after initial filing)			Examiner Name	ALI, S	Syed J.			
Total Number of Pages in This Submission 8			Attorney Dacket Number	mber 42390P12401				
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1 1111110110	INTEL CORPORATION		1					
Signature								
Printed name	Stuart A. Whittington			•				
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REPLY UNDER 37 C.F.R. § 1.116-Expedited Procedure Requested TECHNOLOGY CENTER 2127

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BELOW:

Date of Transmission: 11/1/05

By: Lisa Hopkinson

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

HAYDUK, Matthew A.

Atty. Docket: 42390P12401

Appln. Ser. No.: 10/017,047

Group Art Unit: 2127

Filed: December 13, 2001

Examiner: Ali, Syed J.

For:

COMPUTING SYSTEM CAPABLE OF REDUCING POWER CONSUMPTION BY

DISTRIBUTING EXECUTION OF INSTRUCTION ACROSS MULTIPLE

PROCESSORS AND METHOD THEREFORE

RESPONSE UNDER 37 C.F.R. § 1.116

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In response to the Final Office Action dated August 1, 2005, please consider the following:

Remarks beginning on page 2.